



Scalable Serializer-Deserializer Architecture And Programmable Interface
Om P. Agrawal; Bai Nguyen; Kuang Chi; Brad Sharpe-Geisler; Giap Tran
M-15135 US

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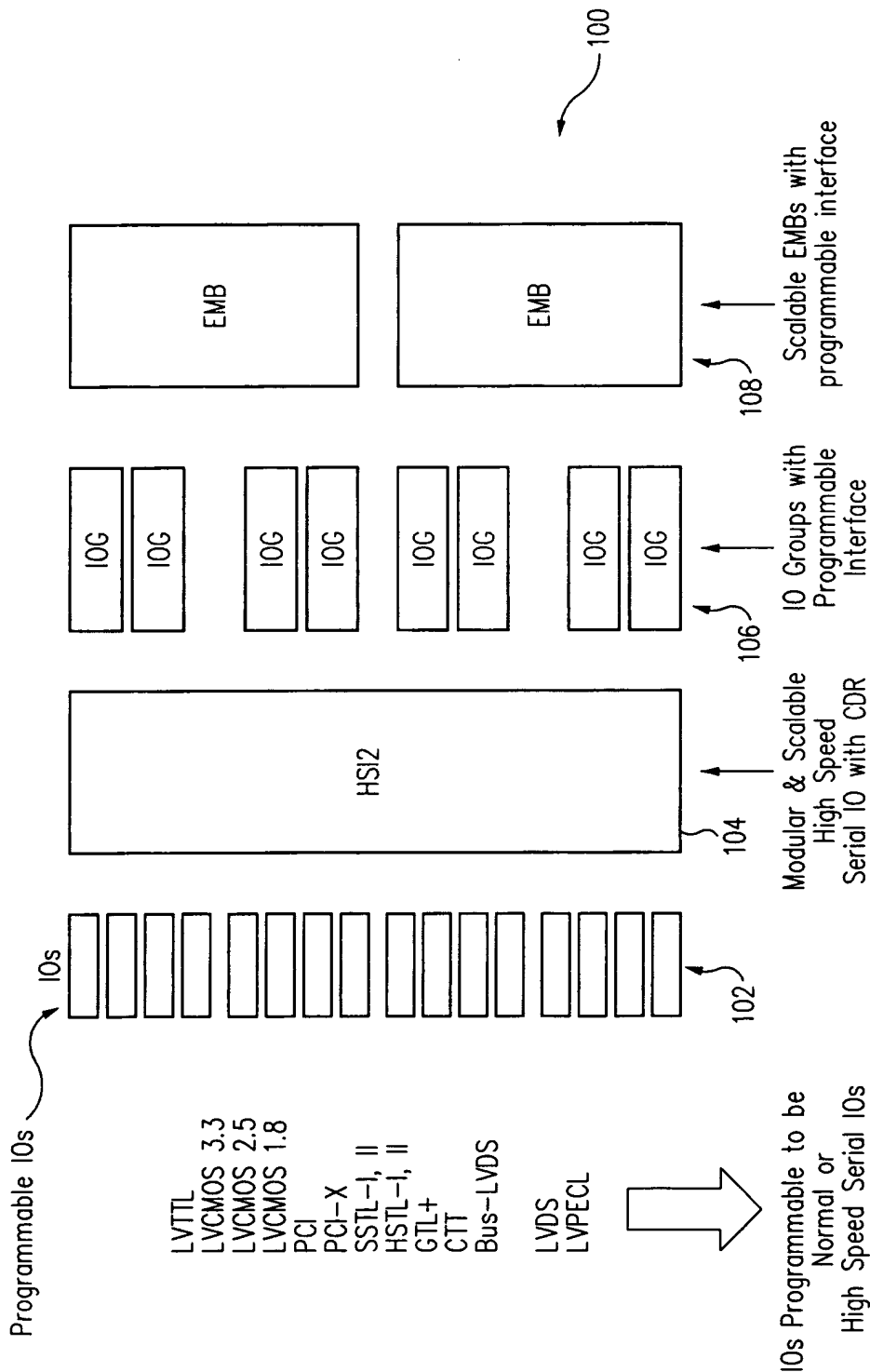


FIG. 1

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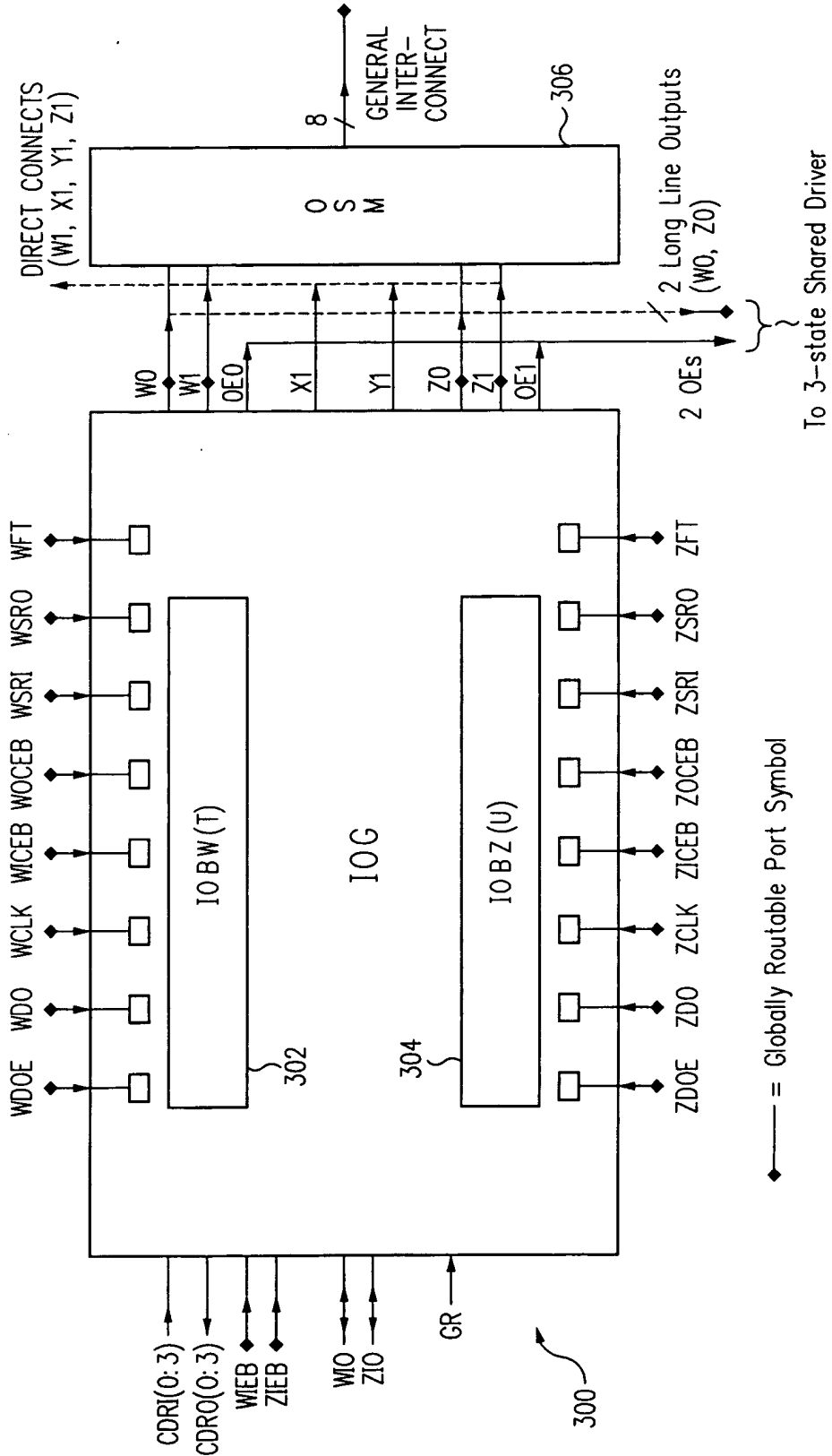
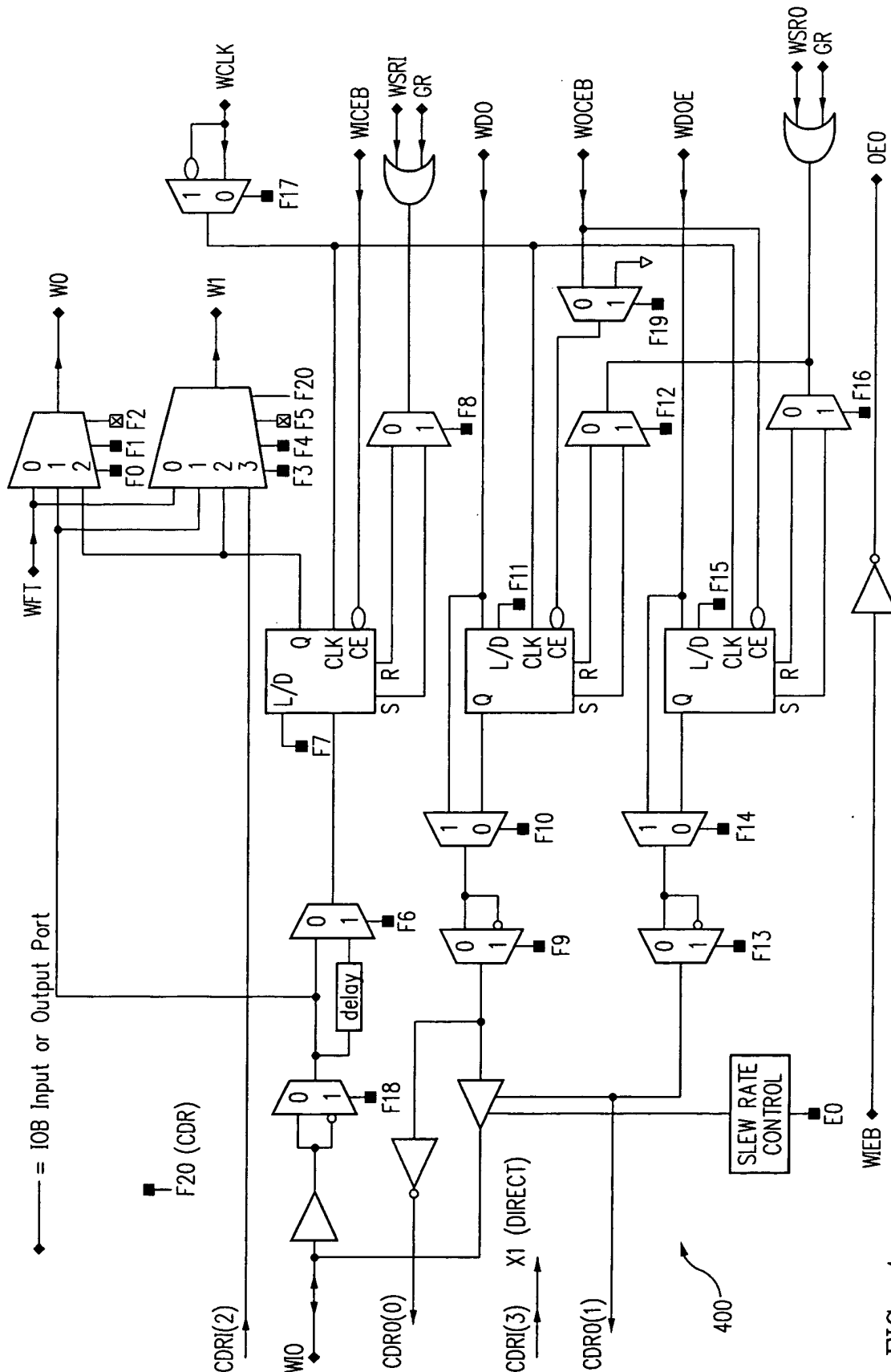


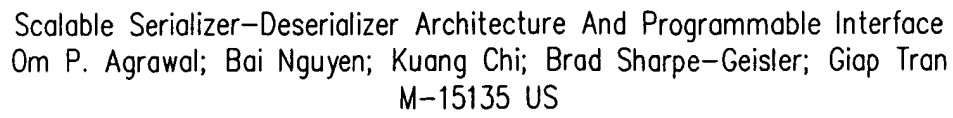
FIG. 3



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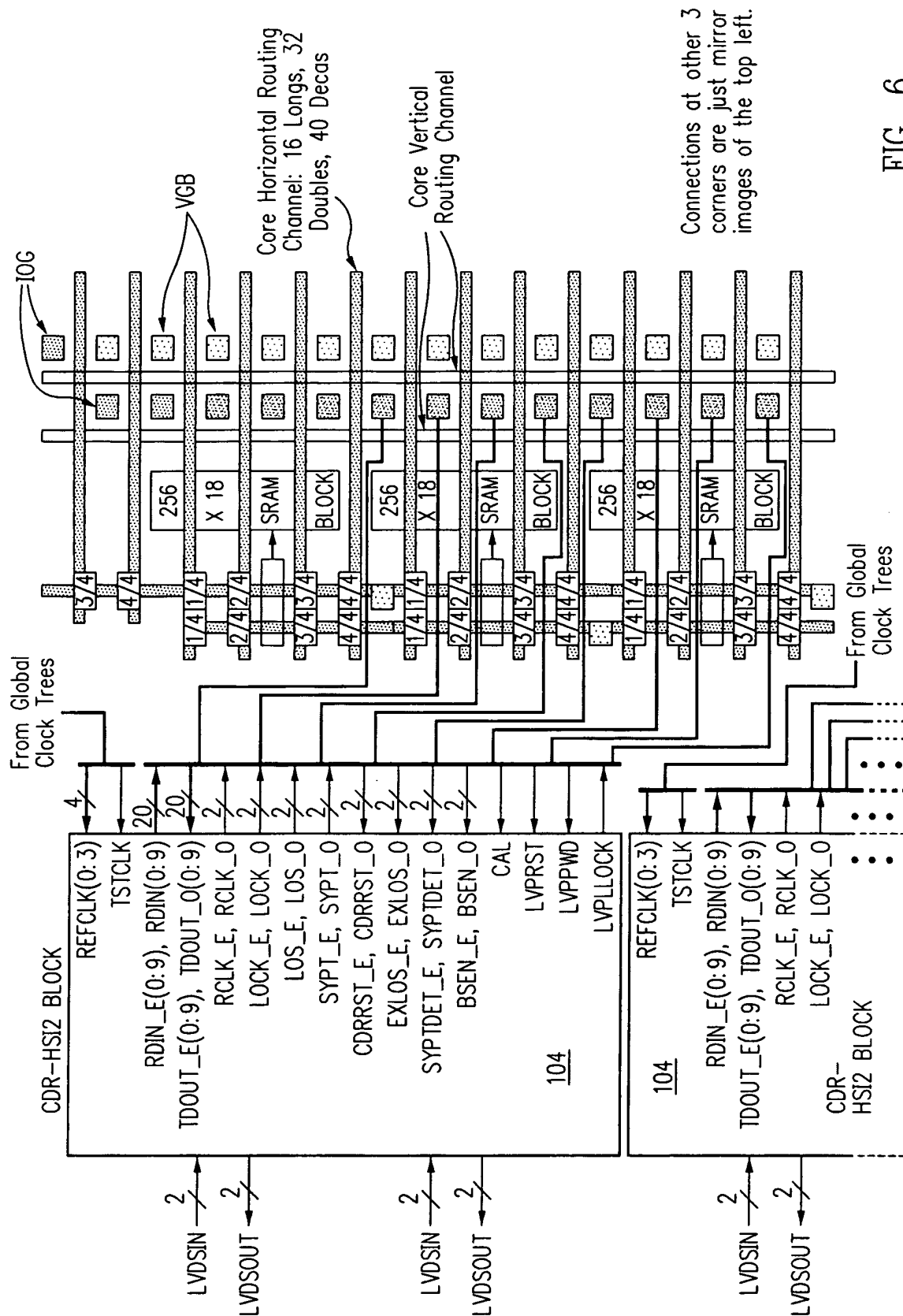
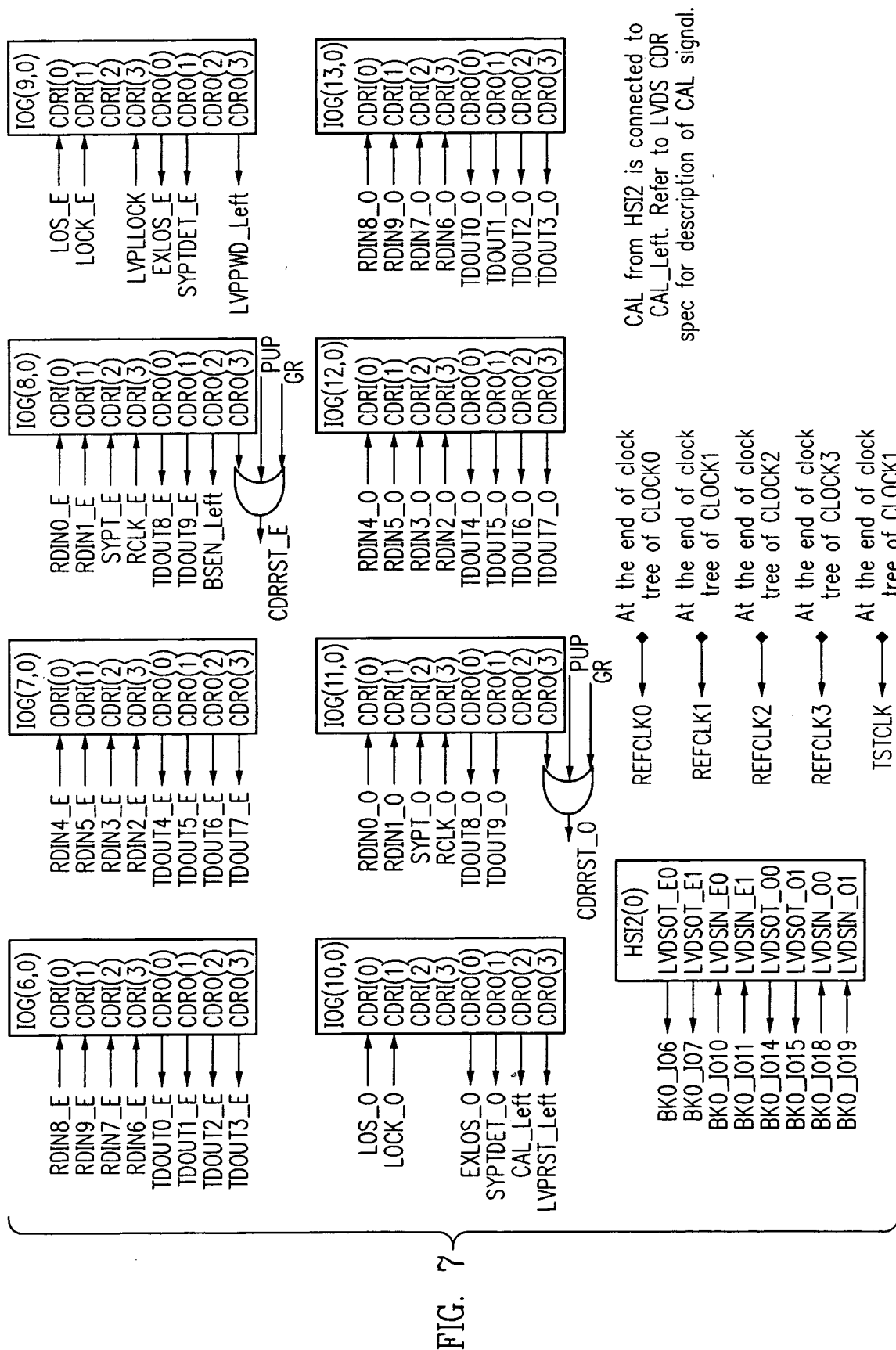


FIG. 6





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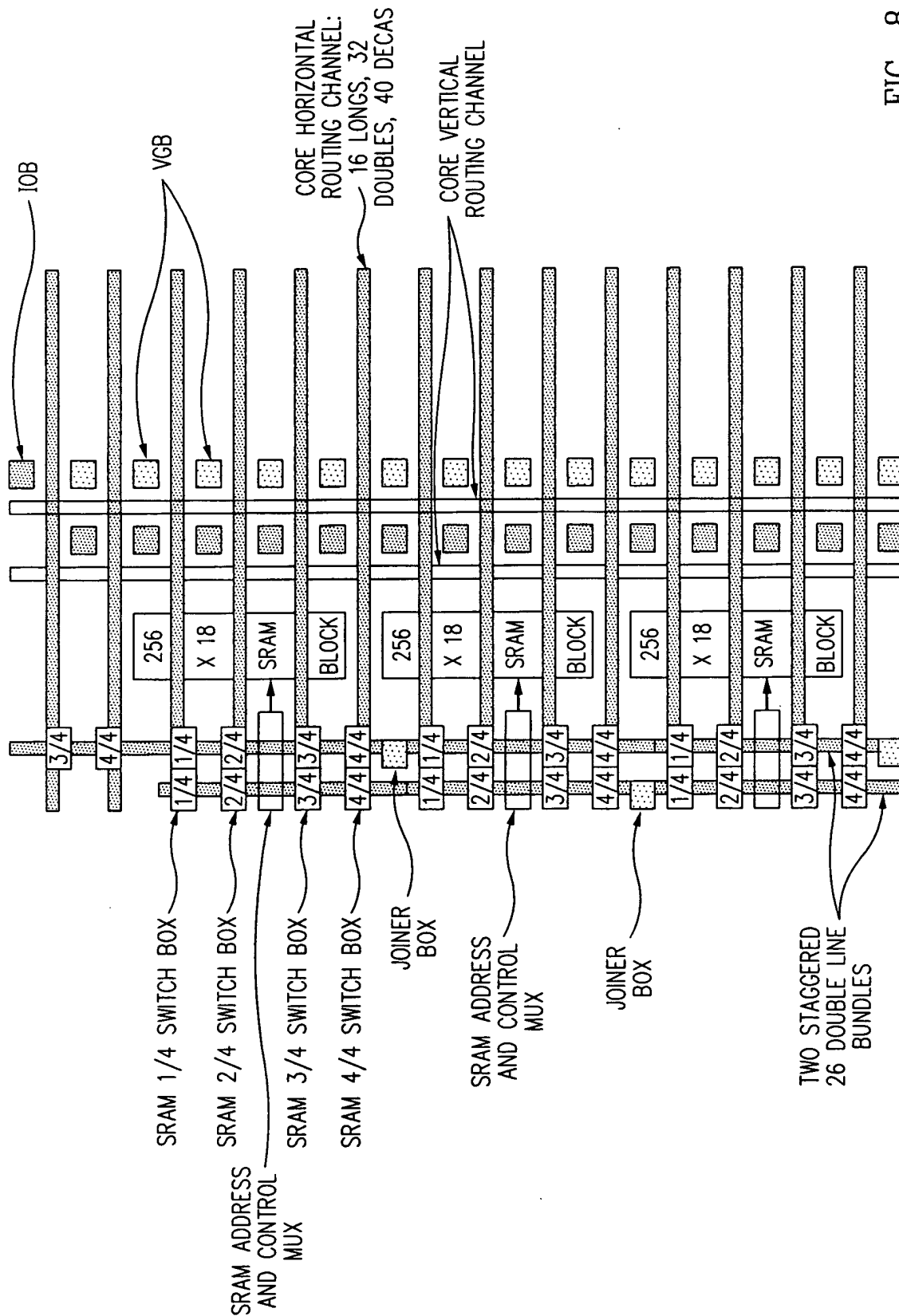


FIG. 8



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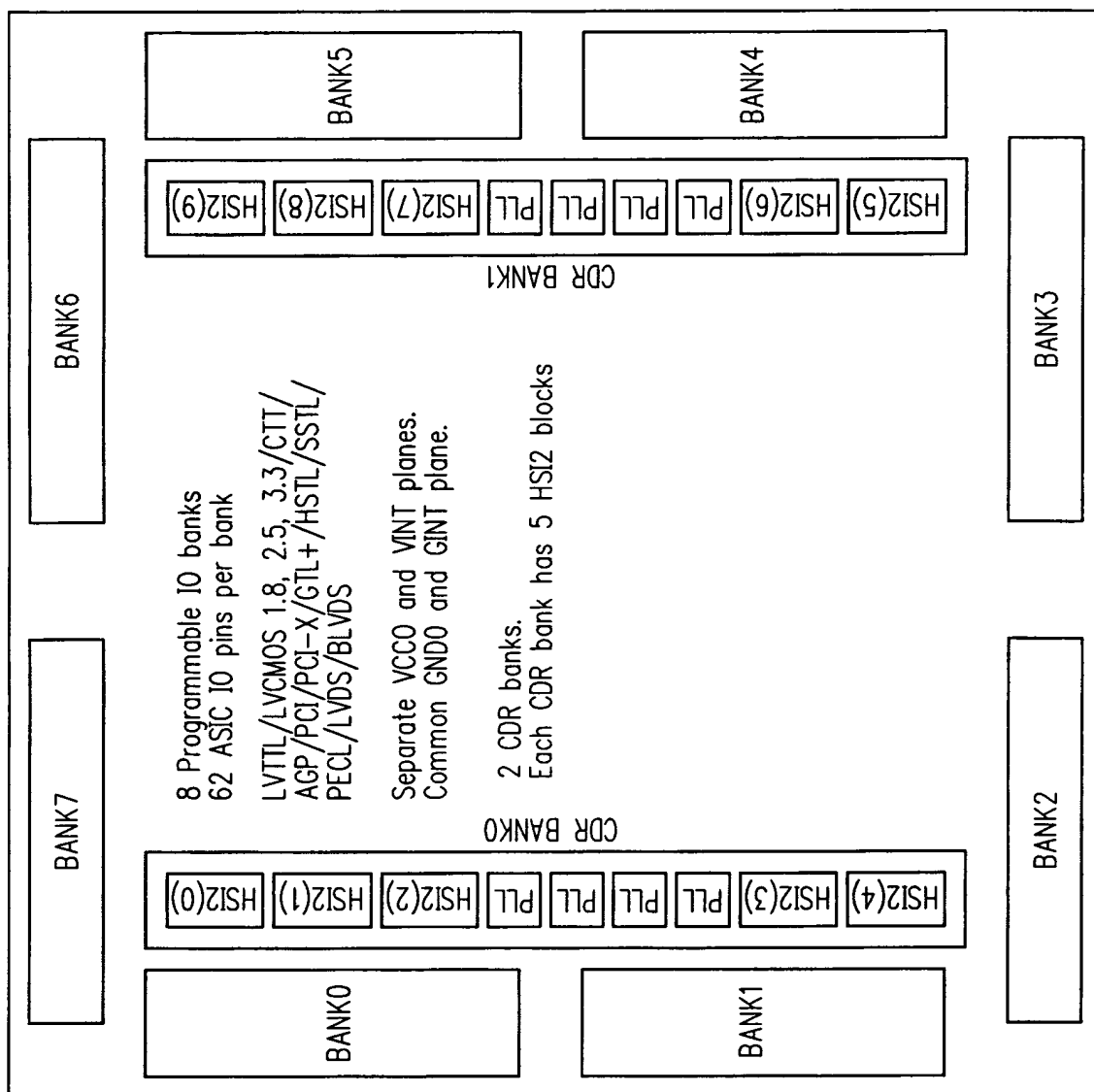


FIG. 9



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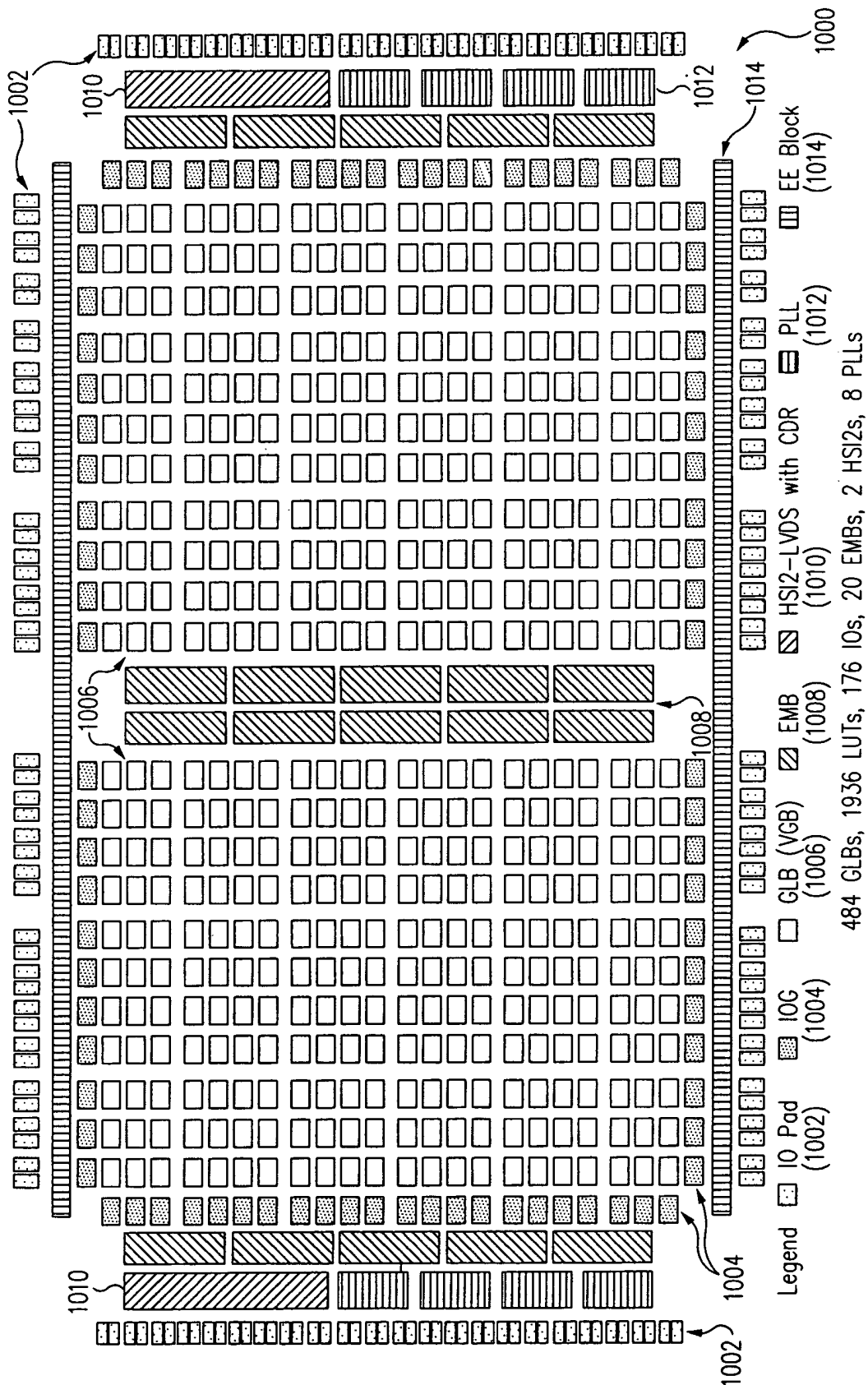


FIG. 10



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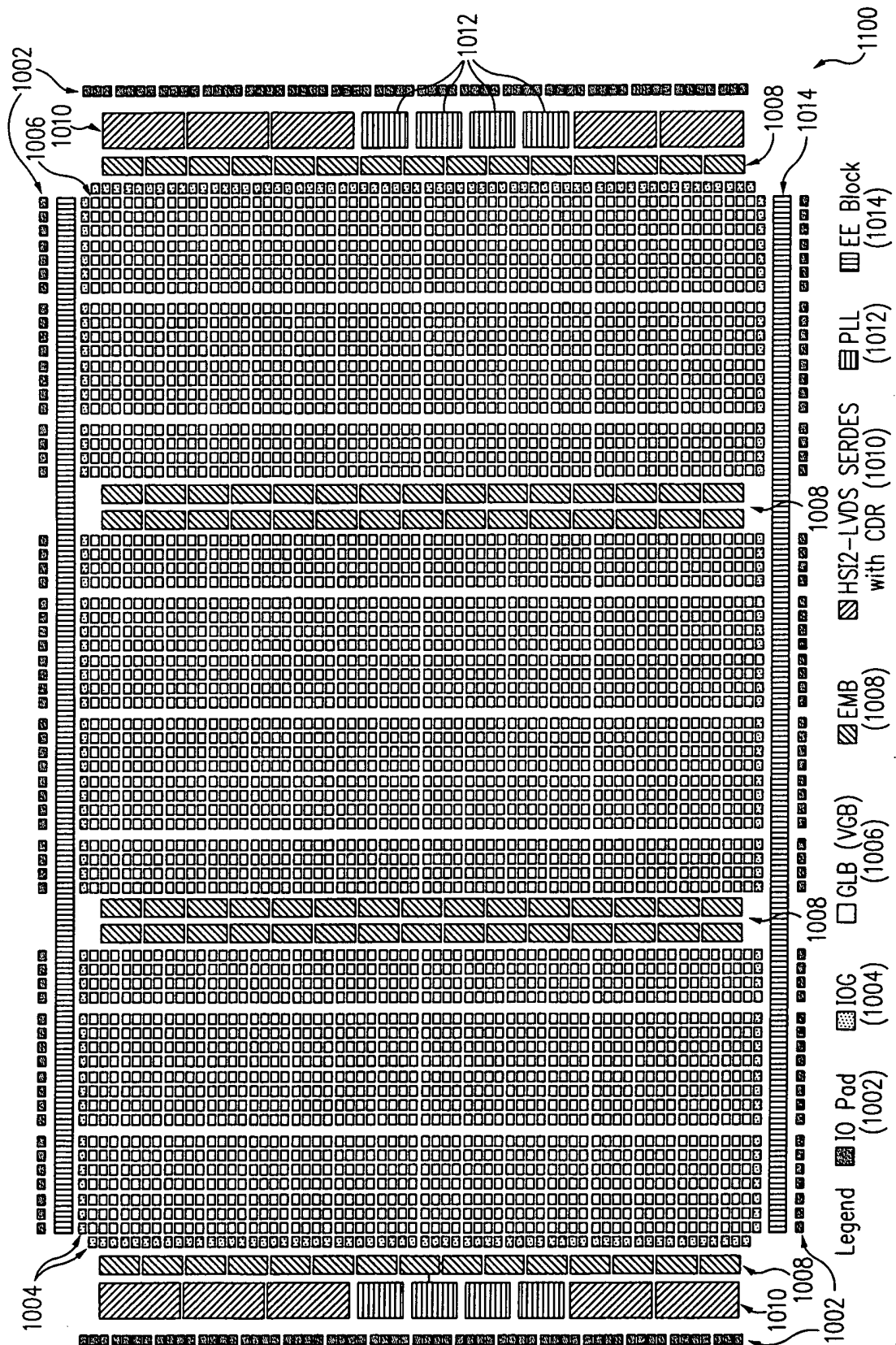


FIG. 11



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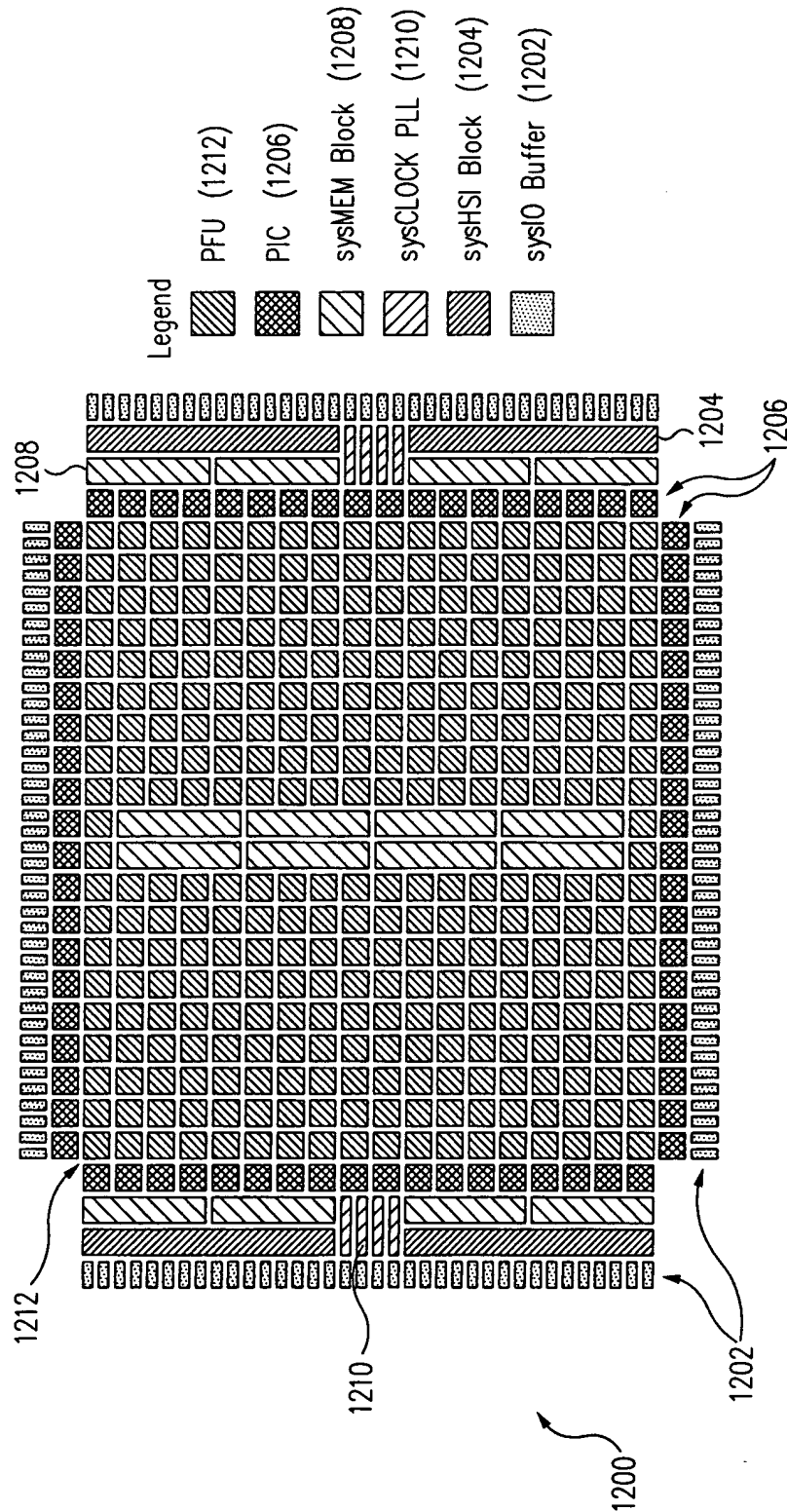
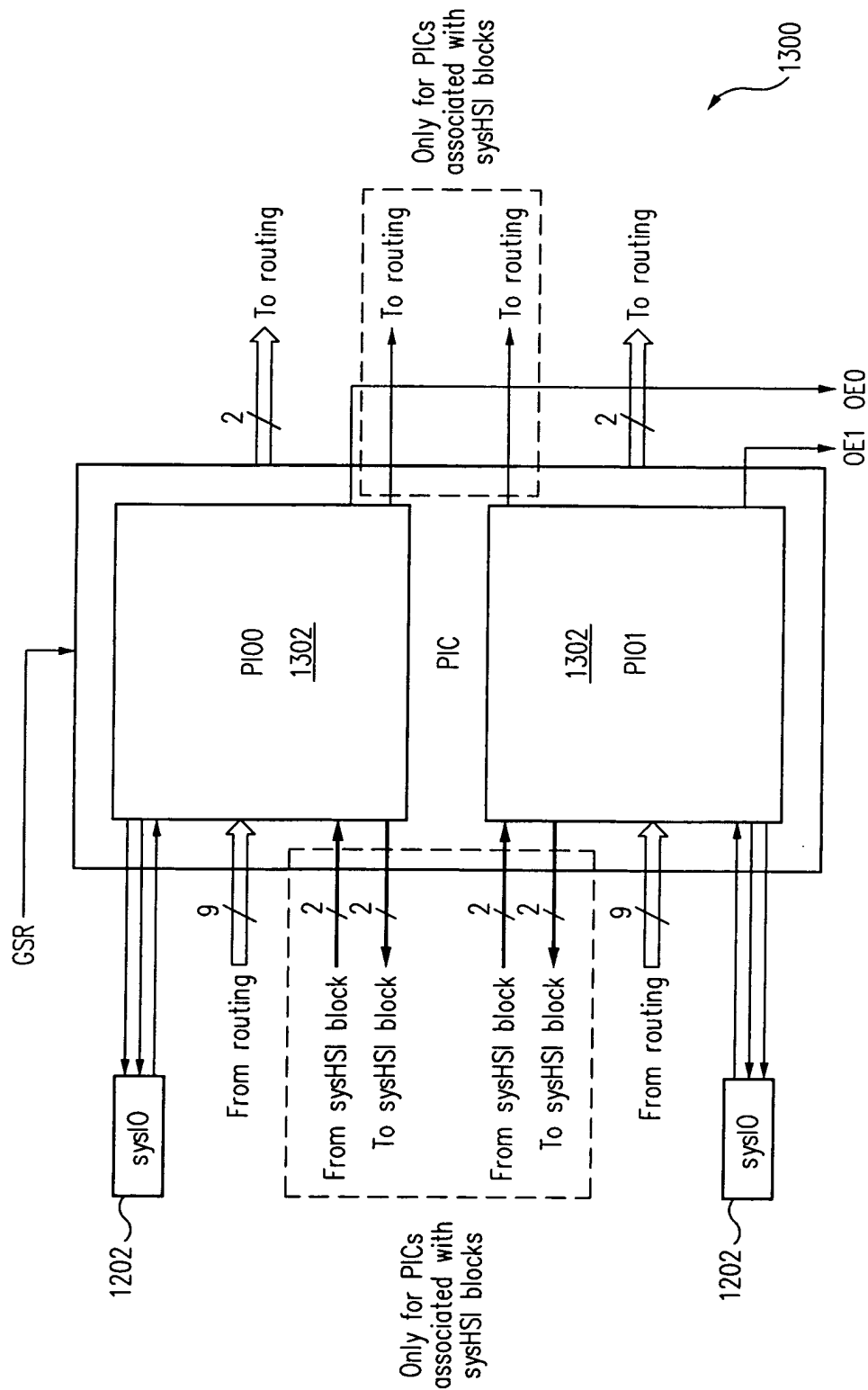


FIG. 12





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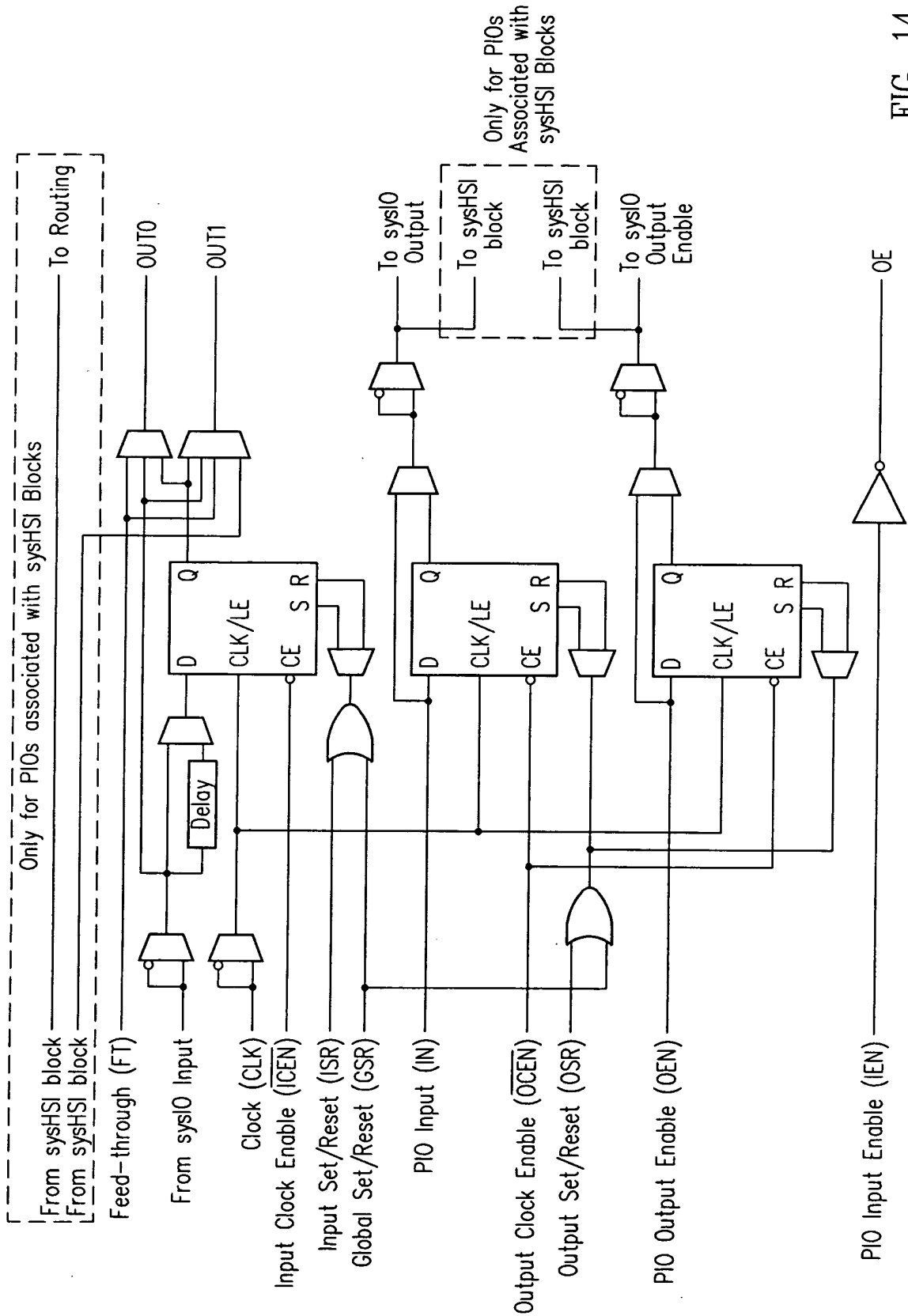


FIG. 14

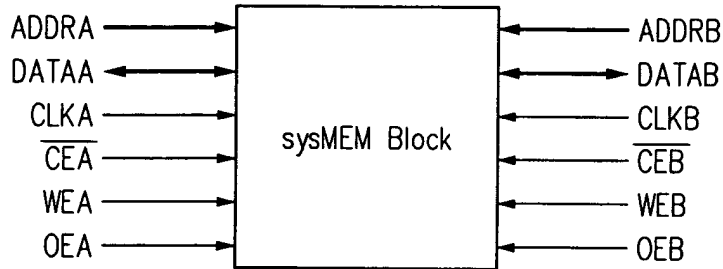


FIG. 15

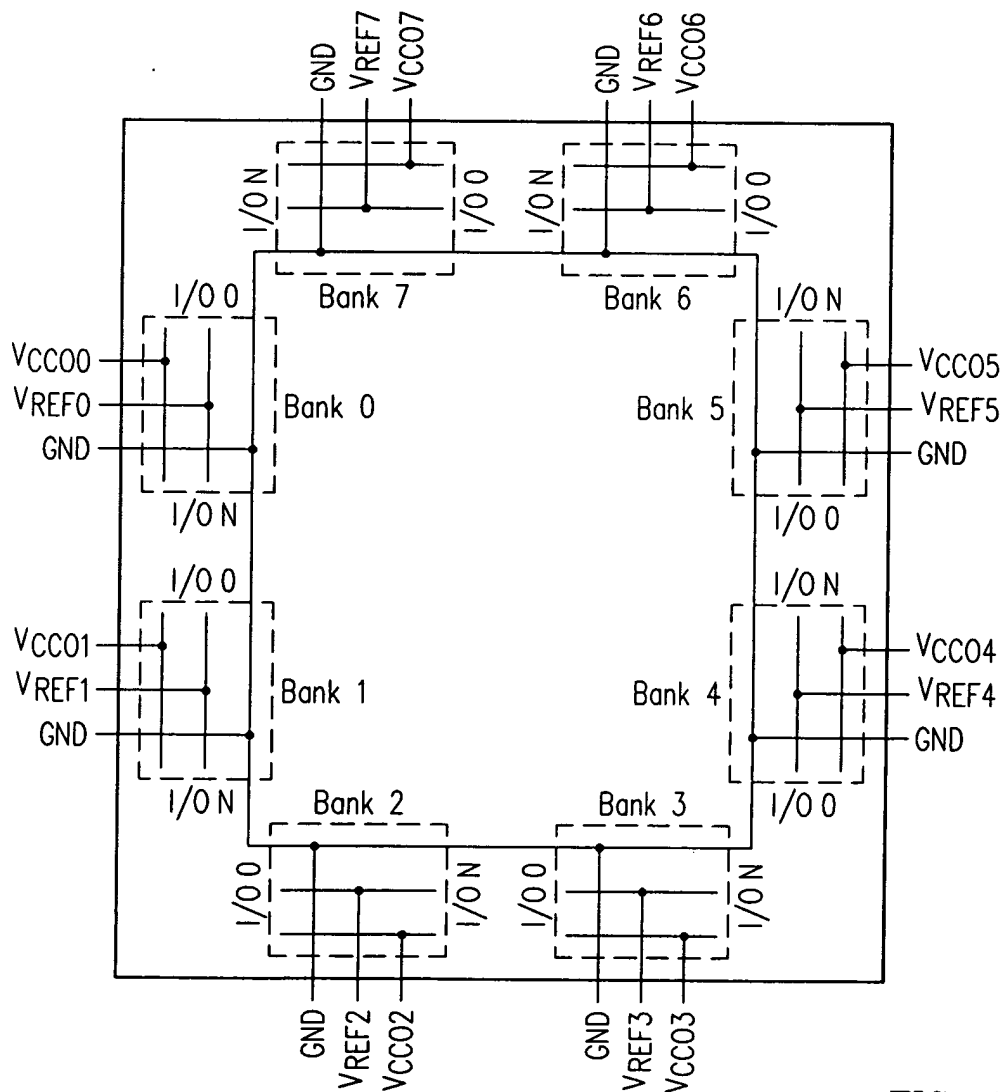


FIG. 16

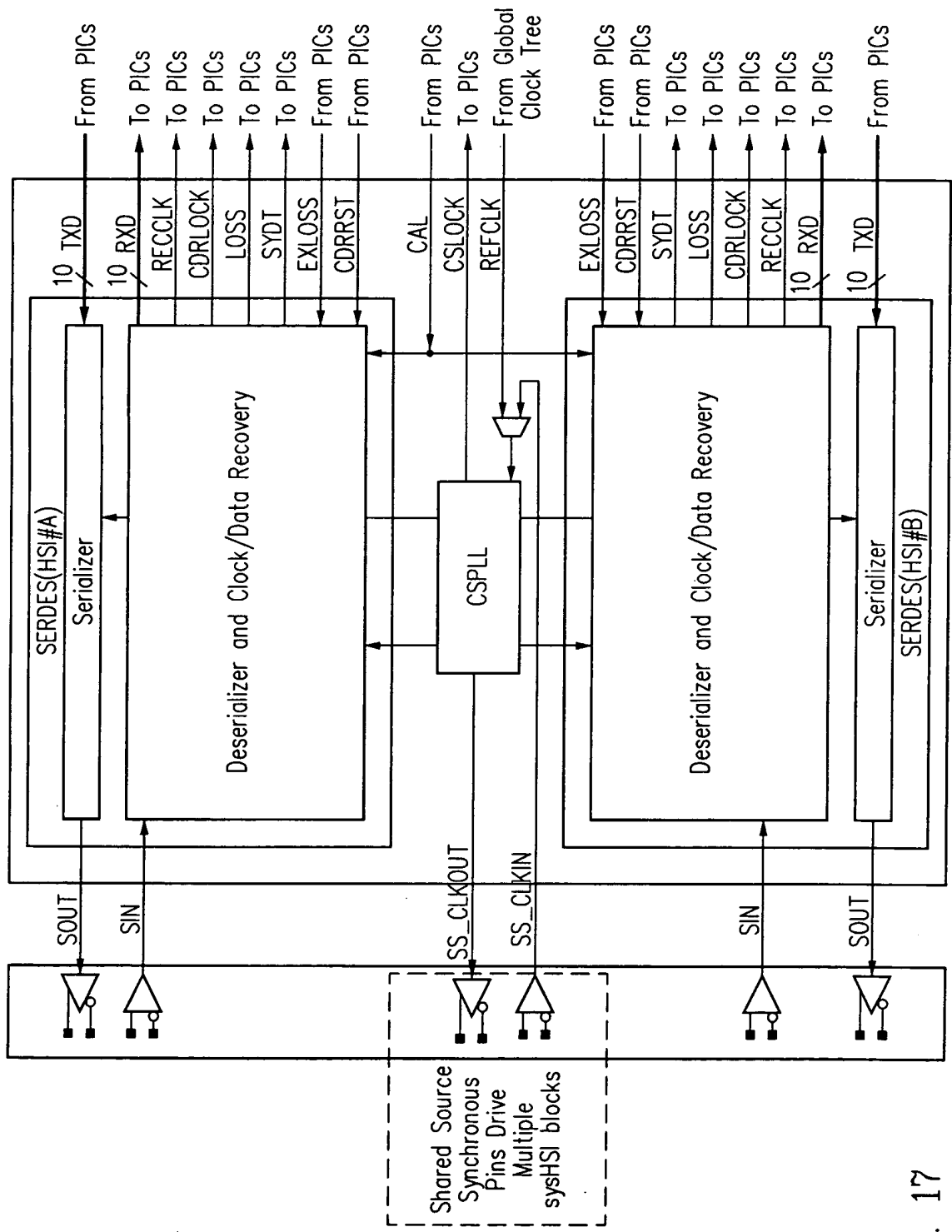


FIG. 17